

3-wire Serial Programming Interface

1. Features

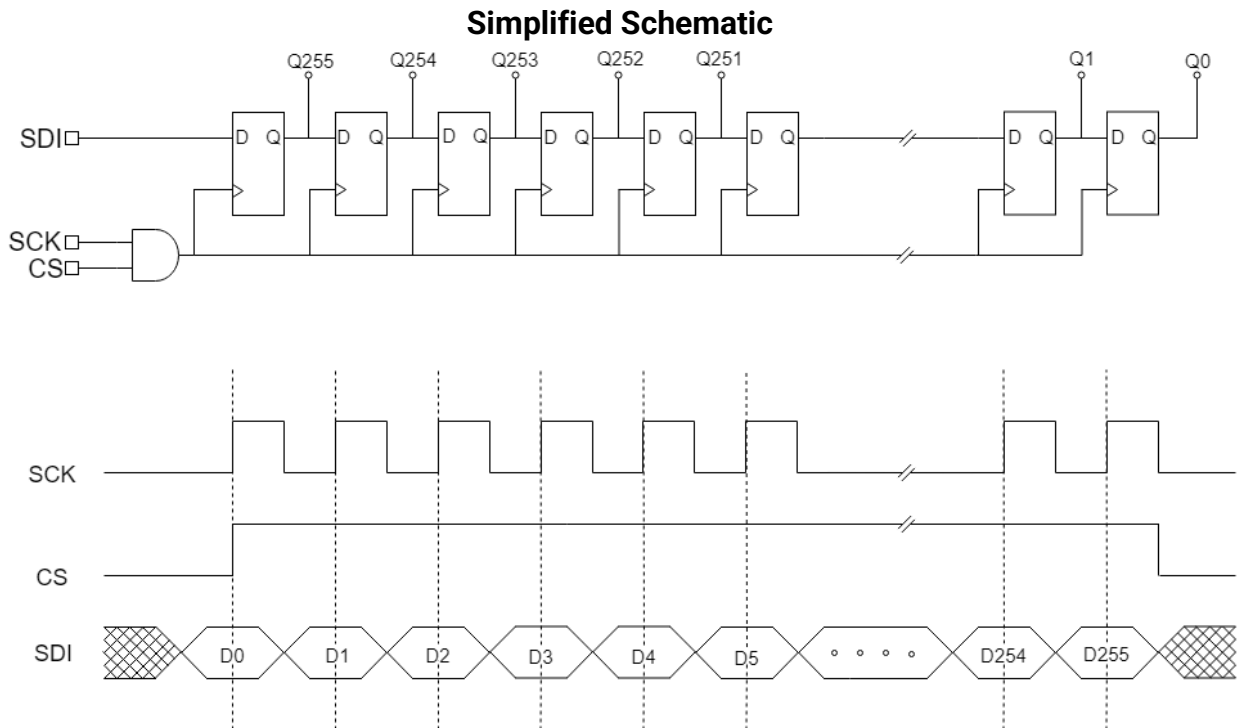
Supply Voltage: 1V
 256 registers
 8-bit Serial – In, Serial Out shift

2. Applications

PLL

3. Description

The WEA1R8SPI55 is a cascade of flip flops, sharing the same clock, in which the output of each flip-flop is connected to the input of the next flip-flop in the chain, resulting in a circuit that shifts by one position the "bit array" stored in it, "shifting in" the data present at its input and 'shifting out' the last bit in the array, at each transition of the clock input. The WEA1R8SPI55 contains an 8-bit serial-in, serial-out shift register that feeds an 8-bit, D-type storage register on the rising edge of the clock (SCK).



4. Availability

GlobalFoundries 45RFSOI
Portable to 40nm, 28nm, 22nm GF





5. Deliverables

GDSII, Database, SystemVerilog Models

About weasic

Weasic Microelectronics S.A. designs, develops, and markets high quality complex analog and RF IP for the wired and the wireless communications industries, helping semiconductor and system companies to shrink the product design cycle. Weasic, silicon verified, IP is designed in the state-of-the-art CMOS and SiGe processes and can be easily ported and customized to serve the development of transceivers for 5G communications, Mobile Backhaul, RADAR sensors and 802.11.* applications.

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